

BIO-DATA



N. SHEHANAZ,

Assistant Professor (Adhoc),

Department of ECE,

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Date of Birth: 08/12/1990

Career Objective

- To make remarkable position by virtue of my education, general exposure by winning the confidence of learned seniors and keeping in mind not to be misled from the path of honesty, hard work and trust.

Educational Qualifications

- Pursuing Ph.D in Dr. A.P.J Abdul Kalam University, Indore, Madhya Pradesh, India.
- **M.Tech (VLSI System Design)** from Annamacharya Institute of Technology & Sciences, Kadapa affiliated by JNTUA during the academic year 2014-2016 with 83.52%.
- **B.Tech (E.C.E)** from Rajoli Veera Reddy Padmaja Engineering College for Women, Kadapa affiliated by JNTUA during academic year 2008-2012 with 77.25%.
- **Intermediate (M.P.C)** from Narayana Junior College, Kadapa during academic year 2006-2008 with 94.6%.

- SSC from Sri Guru Raghavendra High School, Kadapa during academic year 2005-2006 with 82.2%.

Teaching Experience

- Working as Adhoc Assistant Professor at JNTUA College of Engineering (Autonomous), Pulivendula in Electronics and Communication Engineering Department from 5th July 2017 to Till Date.
- Worked as Academic Assistant at RGUKT (IIIT), Idupulapaya, Vempalli in Electronics and Communication Engineering Department from 5th January 2017 to 10th May 2017.
- Worked as Assistant Professor in the Department of Electronics and Communication Engineering in Narayanadri Institute of Science and Technology, Rajampet from 13th July 2016 to 11th November 2016.

Academic Achievements

- Got Elite & Silver Medal in Principles of Signals & Systems in NPTEL Online Certifications held on April 2019.
- Got Consolation Prize for Paper Presentation at “National Conference on Recent Advances in Communication and Electronics Engineering – 2016” at NIT Warangal.
- Participated in the National Level Students Level Technical Symposium Scintillace-2k16 during 14-15th March 2016.
- Got First Position in I & II Semester exams of M.Tech in AITS Kadapa, conducted by JNTU Anantapur held on April 2015 and October 2015.
- Participated in National Level Workshop called “Robosapiens” in JNTU Pulivendula in February 2011.

Additional Duties Handled

- Worked as Project Coordinator in the Department Project Review Committee (UG) in JNTUA College of Engineering Pulivendula.
- Worked as Lab Coordinator for Microprocessors Lab and Class Representative Chairman (CRC) for IV B.Tech II Semester at JNTUACEP.

Subjects Taught

- Embedded Systems & IoT (2 times)
- Satellite Communications (3 times)

- Electronic Measurements & Instrumentation (1 time)
- Television Engineering (1 time)
- Signals & Systems (3 times)
- Micro Processors & Micro Controllers (2 times)
- Embedded Systems (1 time)
- Basic Electrical & Electronics Engineering (2 times)
- Mobile Communications (1 time)

Technical Skills

- | | |
|---|----------------------|
| • MPI programming | • Signal processing |
| • Verilog & VHDL programming | • Analog and Digital |
| • Analog and Digital Electronics design | Communications |

Patent Published: 01

- N. Shehanaz, B.V Sowjanya Published a Patent on “**Smart Parking and License Plate Detection**”, Issue No. 31/2020 with Patent Application Number 202041029576 (Indian Patent), 31-July-2020.

International Journals: 05

- N. Shehanaz, B.V Sowjanya published paper on " **An High Speed Ratioed CMOS Circuit Design With Modified Pseudo Logic Circuits**" in International Journal of Technical Innovation in Modern Engineering & Science (IJTIMES) an UGC approved journal with ISSN: 2455-2585, Volume 5, Special Issue 05, May-2019, PP:- 49-53 with Impact Factor 5.22.
- N. Shehanaz, B.V Sowjanya, T.C Sanjeeva Rayudu published paper on "**An Efficient and Improved Radix-16 Based Booth Multiplier for 64-Bit Based on the Reduced Height of Partial Product Array**" in Journal of Emerging Technologies and Innovative Research (JETIR), an UGC approved journal with ISSN: 2349-5162, Volume 5, Issue 5, May 2018, PP:174-178 with Impact Factor 5.87.
- N. Shehanaz, M. Shalima Sulthana published paper on “**Simulation of Power Tracking Scheme through Excitation Synchronous Wind Power**” was published in International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE) with ISSN: 2278-8875, Volume 6, Issue 2, February 2017, PP: 677-687, with Impact Factor 5.621.

- N. Shehanaz, M. Shalima Sulthana published paper on **“A Novel Frame Work Secure and Reliable Data Routing in WSN (Wireless Sensor Network)”** was published in International Journal of Computer Science Engineering and Scientific Technology (IJCSEST) with ISSN: 6201 3454, Volume 3, Issue 4, February 2017, with Impact Factor 3.5.
- N. Shehanaz, Dr. T. Lalith Kumar published paper on **“An Implementation of Area Efficient & Fast Addition and Multiplication Operation using Radix based Modified Booth Recoding Technique”** was published in International Journal of Emerging Technology in Computer Science & Electronics (IJETCSE) with ISSN: 0976-1353, Volume 22, Issue 2, May 2016, PP:504-507, with Impact Factor 1.012.

Conferences attended: 03

- Presented a paper on **“A High Speed Ratioed CMOS Circuit Design with Modified Pseudo Logic Circuits”** at National Conference on Innovative Technologies in Electrical Engineering (NCITEE)-2k19 organized by Department of Electrical and Electronics Engineering at Geetanjali Institute of Science and Technology, Nellore held on 30th March 2019.
- Presented a paper on **“An Efficient System Design for 32 Bit Sum Product Operator in Modified Booth Form using Fusion Technique”** at National Conference on Recent Advances in Communication and Electronics Engineering-2016 at NIT Warangal held from 21st – 22nd October 2016.
- Presented a paper on **“An Implementation of Area Efficient & Fast Addition and Multiplication Operation using Radix based Modified Booth Recoding Technique”** International Conference on Advances in Engineering and Technology (ICAET-16) at Jaya Engineering College, TamilNadu on 07th May 2016.

Expert Lecture Delivered: 02

- Delivered a lecture on **“Embedded System Fundamentals with ARM Microcontrollers”** as a part of Seminar organized by Institution of Engineers (India), Kadapa Local Centre on 19th November 2022.
- Delivered a lecture on **“Recent Trends in VLSI Chip Design”** as a part of Seminar organized by Institution of Engineers (India), Kadapa Local Centre on 15th October 2022.

FDP's/Workshops/Seminars attended

- Five days National online FDP on “Intellectual Property Rights” Organized by MGRERI, Tamilnadu during 22nd to 26th May 2023.
- One week IEEE BS Sponsored online FDP (Online Capacity Building Workshop) on “Blooming Research Areas Under IoT: A 360-Degree View” Organized by FGCOE, Jalgaon during 02nd to 07th May 2023.
- One week International online FDP on “Block Chain Technology & it’s Applications” Organized by VMEG, Hyderabad during 25th to 29th April 2023.
- 30 days APSSDC Sponsored Online FDP on “Artificial Intelligence 3.0” Organized by Pantech e Learning Pvt Ltd, during 24th April 2023 to 23th May 2023.
- 30 days APSSDC Sponsored Online FDP on “REACT JS” Organized by Pantech e Learning Pvt Ltd, during 10th April 2023 to 12th May 2023.
- 30 days APSSDC Sponsored Online FDP on “Embedded Systems” Organized by Pantech e Learning Pvt Ltd, during 27th March 2023 to 27th April 2023.
- 30 days APSSDC Sponsored Online FDP on “Python Full Stack Development” Organized by Pantech e Learning Pvt Ltd, during 27th March 2023 to 27th April 2023.
- 30 days APSSDC Sponsored Online FDP on “Python Full Stack Development” Organized by Pantech e Learning Pvt Ltd, during 28th Feb 2023 to 29th March 2023.
- 30 days APSSDC Sponsored Online FDP on “Web Development using REACT JS” Organized by Pantech e Learning Pvt Ltd, during 04th January 2023 to 03rd February 2023.
- 30 days APSSDC Sponsored Online FDP on “Artificial Intelligence Master Class” Organized by Pantech e Learning Pvt Ltd, during 23nd November 2022 to 22nd December 2022.
- 30 days APSSDC Sponsored Online FDP on “Image Processing using OpenCV Master Class” Organized by Pantech e Learning Pvt Ltd, during 12th October 2022 to 11th November 2022.
- 30 days APSSDC Sponsored Online FDP on “Data Science 2.0 Master Class” Organized by Pantech e Learning Pvt Ltd, during 20th Sep 2022 to 21st Oct 2022.
- One week AICTE sponsored online FDP on “Amazon Web Services” Organized by KSRM(A), Kadapa during 22nd to 27th August 2022.

- Five days National level online FDP on “Current Research Trends in Artificial Intelligence & Machine Learning” Organized by GIT, Gooty during 7th to 11th March 2022.
- One week AICTE sponsored online STTP on “Practical Exposure on Machine Learning & Data Science (Phase-III)” Organized by GPREC (A), Kurnool during 13th to 18th September 2021.
- One week AICTE sponsored online STTP on “Emerging Trends in Advanced Signal & Image Processing (Slot-3)” Organized by MRECW, Secunderabad during 19th to 24th April 2021.
- One week AICTE sponsored online STTP on “Emerging Trends in Advanced Signal & Image Processing (Slot-1)” Organized by MRECW, Secunderabad during 15th to 20th April 2021.
- One week AICTE sponsored online STTP(Phase-1) on “VLSI Design and Verification” Organized by AITS, Rajampet during 22nd to 27th March 2021.
- One week AICTE sponsored online STTP on “Multi-Objective Optimization Methods for Engineering and Scientific Applications” Organized by SSTC, Bhilai during 29th January 2021 to 3rd February 2021.
- One week AICTE sponsored online STTP on “Hands on Training in Artificial Intelligence & Optimization technique applications for Additional lab experiments and mini, major for BTech Engineering students” Organized by VJIT(A), Hyderabad during 07th to 12th December 2020.
- One week AICTE sponsored online FDP on “Quantum Computing and Machine Learning” Organized by JNTUACEP and Directorate of Faculty Development & IQAC, JNTU, Ananthapuramu during 30th November 2020 to 05th December 2020.
- One week e-STTP on “Recent Trends in Instrumentation, Measurement & Automation” Organized by EG, Ajmer in Twinning with VJTI, Mumbai under TEQIP-3 during 26th to 30th October 2020.
- Five days ATAL Academy Sponsored online FDP on “Control Systems & Sensors Technology” Organized by Madan Mohan Malaviya University of Technology, Gorakhpur during 17th to 21st October 2020.
- 20 days APSSDC Sponsored Online FDP on “R Programming” Organized by 360DigiTMG during 07th September 2020 to 05th October 2020.

- Three days Online FDP on “Enhancement of Professional Communication Skills” Organized by ALITS, Ananthapuramu during 20th to 22nd August 2020.
- Five days IEI Sponsored online FDP on “VLSI & Embedded Systems Design for Aerospace & Defence Applications” Organized by SVIST, Kadapa during 10th to 14th August 2020.
- One week online FDP on “Python Programming -II” Organized by ALITS, Ananthapuramu in association with Spoken Tutorial IIT, Mumbai during 03rd to 08th August 2020.
- One week online FDP on “Recent Advances in Embedded Systems” Organized by MGIT, Hyderabad during 07th to 11th August 2020.
- One week Online FDP on “Simulink: An Overview” Organized by DataTeach Labs during 25th to 30th June 2020.
- One week online FDP on “Introduction to Renewable Energy Sources” Organized by KITS, Guntur during 20th to 24th June 2020.
- Five days online FDP on “Research Trends in Image Processing” Organized by Gates Institute of Technology, Ananthapuramu during 15th to 19th June 2020.
- One day Webinar on “Design and Fabrication of Low Cost Microfluidic Device for Polymerase Chain Reaction (PCR)” Organized by Maratha Mandal Engineering College, Karnataka on 15th June 2020.
- One week International online Knowledge Develop Program on “Challenges & Advancements in the Design of IoT, Embedded and VLSI Systems: A Researchers View” Organized by GEC, Gulavalleru during 08th to 13th June 2020.
- One week online FDP on “Recent Trends & Research Areas in Applied VLSI and Advanced Communications” Organized by VVIT, Guntur during 08th to 12th June 2020.
- One week online FDP on “Recent Trends in Communication and Wireless Networks” Organized by KITS, Guntur during 5th to 9th June 2020.
- 2 weeks Online FDP on “Matlab Masters” Organized by DataTeach Labs during 08th to 19th June 2020.
- Two days APSSDC Sponsored Online FDP on “MATLAB and Simulink in Engineering Education” Organized by APSSDC during 10th to 11th June 2020.
- One day Webinar on “Free Space Optics” Organized by SVCE, Nellore on 09th June 2020.

- One day Webinar on “Simulation Driven Innovation for e-Mobility” Organized by ALTAIR, India on 09th June 2020.
- One day Webinar on “Impact on Covid-19 on Engineering Education & A Way Forward” Organized by NRI Institute of Technology, Guntur in association with Efftronics on 08th June 2020.
- One week online FDP on “Research Challenges & Emerging Trends in Electronics and Communication Engineering (RCEECE-2020)” Organized by Ramachandra College of Engineering, Eluru during 27th May 2020 to 01st June 2020.
- 2 weeks Online FDP on “Matlab Tools & their Applications in Engineering” Organized by DataTeach Labs during 25th May 2020 to 07th June 2020.
- 2 days Online FDP on “Essential Matlab for Engineers” Organized by DataTeach Labs during 29th to 30th May 2020.
- Three days Webinar FDP on “IoT with Artificial Intelligence” in Collaboration with 360DigiTMG Organized by Sir C R Reddy College of Engineering, Eluru during 27th to 29th May 2020.
- Five days online FDP on “Recent Communication Tools and it’s Application” Organized by St. Martin’s Engineering College (A), Secunderabad during 19th to 23rd May 2020.
- Two days online Workshop on “VLSI Circuits Analog & Digital Design Perspectives” Organized by JNTU, Ananthapuramu in association with AMS Semiconductors & HCL Technologies during 16th to 17th May 2020.
- One day workshop on “Awareness on NPTEL Courses” conducted by IIT Madras at JNTUA College of Engineering Pulivendula on 15th December 2018.
- Six days workshop on “Embedded Systems (IOT)” conducted by APSSDC at JNTUA College of Engineering Pulivendula from 21st to 26th September 2018.
- Two-day National Level Students Technical Symposium “Scintillace2k14” by Department of Electronics and Communication Engineering at JNTUA College of Engineering Pulivendula from 14th to 15th March 2016.
- One day seminar on “Recent Trends in Communication system design” conducted by AKADEMIKA lab solutions, Navi Mumbai at AITS Kadapa on 14th July 2015.
- Two-day National Level Robotics Workshop and Championship on “Sat Bot - Tricks” Roboopus-2011 in association with Robosapiens India organized by Technex'2011 IT-

BHU in Department of Electronics and Communication Engineering, JNTUA College of Engineering Pulivendula from 10th to 11th March 2011.

Areas of Interest

- VLSI Chip Design
- Robotics
- Communications

Strengths

- Hardworking and self-motivated nature towards achieving my targets
- Capacity to work under pressure
- Quick learner with ability to grasp new ideas and concepts
- Flexibility approach towards work
- Good communication skills
- Crisis management

Personal Profile

Name	:	N. Shehanaz
Father's Name	:	N. Gaffer
Mother's Name	:	N. Muntaz Begum
Gender	:	Female
Date of Birth	:	08.12.1990
Nationality	:	Indian
Languages Known	:	English, Telugu, Hindi and Urdu.

Declaration

I hereby declare that the above given information is true to the best of my knowledge.

Place: Kadapa

Date:

(N.SHEHANAZ)